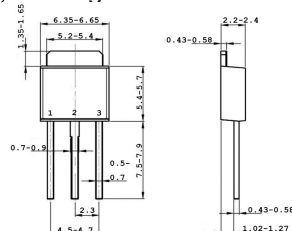
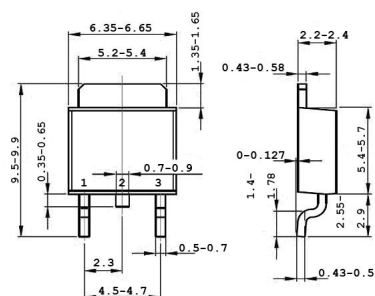


TRIAC
Plastic-Encapsulate Thyristor
FEATURES

- Passivated triacs in plastic envelope, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance.
- Typical applications include motor control, industrial and domestic lighting, heating and static switching.

TO-251

TO-252-2L

1. ANODE
2. ANODE
3. GATE


MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

MAXIMUM RATINGS

Parameters	Symbols	Conditions	Value	UNITS
Repetitive Peak Off-State Voltages	V_{DRM}		600	V
RMS On-State Current Non-Repetitive Peak On-State Current	$I_{T(RMS)}$	Full sine wave; $T_{mb} \leq 107^\circ\text{C}$	4	A
I^2t for Fusing	I^2t	$t=10\text{ms}$	3.1	A^2s
Repetitive Rate of Rise of On-State Current after Triggering	di_T/dt	$di_G/dt=0.2\text{A}/\mu\text{s}$ T2+G+ T2+G- T2-G- T2-G+	50 50 50 10	$\text{A}/\mu\text{s}$ $\text{A}/\mu\text{s}$ $\text{A}/\mu\text{s}$ $\text{A}/\mu\text{s}$
Peak Gate Current	I_{GM}		2	A
Peak Gate Voltage	V_{GM}		5	V
Peak Gate Power	P_{GM}		5	W
Average Gate Power	$P_{G(AV)}$	Over any 20ms period	150	W
Storage Temperature	T_{STG}		-40-150	$^\circ\text{C}$
Operating Junction Temperature	T_J		125	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS

Parameters	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Rated Repetitive Peak Off-State Current	I_{DRM}	$V_D=V_{DRM}$			10	μA
On-State Voltage	V_{TM}	$I_T=5\text{A}$		1.4	1.7	V
Gate Trigger Current	I_{GT}	$V_D=12\text{V}$ $R_L=100\Omega$		5 8 11 30	25 25 25 70	mA
Latching Current	I_L	$V_D=12\text{V}$ $R_L=100\Omega$		7 16 5 7	20 30 20 30	mA
Holding Current	I_H	$V_D=12\text{V}, I_G=0.1\text{mA}$		5	15	mA
Gate Trigger Voltage	V_{GT}	$V_D=12\text{V}, R_L=100\Omega$		0.7	1.5	V
Thermal Resistance Junction to Mounting Base	$R_{th j-mb}$	full cycle half cycle			3.0 3.7	k/W
Thermal Resistance Junction to Ambient	$R_{th j-a}$	In free air		60		k/W